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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/567,488	03/06/2006	Takeshi Kobayashi	126923	1001

25944 7590 06/13/2006

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EXAMINER

HITESHEW, FELISA CARLA

ART UNIT	PAPER NUMBER
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1722

DATE MAILED: 06/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/567,488	KOBAYASHI, TAKESHI	
	Examiner	Art Unit	
	Felisa C. Hiteshew	1722	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 17-36 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 17-20, 22-30 and 35 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>see attached paper</u> . | 6) <input type="checkbox"/> Other: ____. |

Priority

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

The PTOL-1449 has been received.

Claim Rejections - 35 USC § 103

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 17-30,33 and 35 are rejected under 35 U.S.C. 103(a) as being unpatentable over Japanese 2001-053078.

Japanese 2001-053078 teaches a method of producing a silicon wafer which is sliced off from a silicon single crystal ingot, ground and polished, is subjected to an IG processing method where it is quickly heated with a temperature rise rate of 10°C/minute in the temperature range from a room temperature to 800 to 900°C and left to stand for 0.5 to 30 minutes. An ingot is pulled up from a silicon melt so as to be formed of a perfect region [P] where no hole defect aggregate and no interstitial silicon

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defect aggregate exit. The ingot, which is pulled up from a silicon melt, free from dislocations, and contains oxygen deposits. The temperature range from a room temperature to 650°C for 0 to 3 hours, then held at 700 to 800°C for 3 to 10 hours, and then cooled down to a room temperature.

The difference being that Japanese 2001-053078 does not teach a method for producing a wafer wherein the heat-treated ingot is process into mirror-like wafers. However, in the absence of unobvious results, it would have been obvious to one of ordinary skill in the art to modify and optimize the process parameter limitations in order to ensure proper orientation.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Felisa Hiteshew whose telephone number is (571) 272-1463. The examiner can normally be reached on Mondays through Thursday from 5:30 AM to 4:00 PM with Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Yogendra Gupta, can be reached on (571) 272-1316. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-1463.


Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system. see

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<http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866- 217-9197 (toll-free).



FELISA HITESHEW
PRIMARY EXAMINER
AU 1722